

Patent

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Assignee: Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Nhon QUACH et al.
SERIAL NO. : 10/634,899
FILED : August 6, 2003
FOR : APPARATUS AND METHOD FOR PROTECTING CRITICAL
RESOURCES AGAINST SOFT ERRORS IN HIGH
PERFORMANCE MICROPROCESSORS
GROUP ART UNIT : 2114
EXAMINER : Nadeem IQBAL

M/S: AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO NON-COMPLIANT AMENDMENT
UNDER 37 CFR § 1.121

Dear Sir:

The following amendments and remarks below are respectfully submitted in response to the Office Action dated November 9, 2007 and the Notice of Non-Compliant Amendment dated February 6, 2008.

Amendments to the Claims are reflected in the listing of claims which begin on page 2 of this paper.

Remarks/Arguments begin on page 9 of this paper.